



2127

**PATENT**  
Attorney Docket No. 2721

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

WILLMAN, Bryan M.

Group Art Unit: 2127

Serial No. 09/915,628

Examiner: Tang, Kenneth

Filed: July 26, 2001

For: Code and Thread Differential Addressing via Multiplex Page Maps

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Alexandria, VA 22313-1450

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, the references listed on the enclosed Form PTO-1449 are submitted herewith for consideration by the Examiner in the examination of the above-identified patent application. The full consideration of the references in their entirety by the Examiner is respectfully requested and encouraged. Also, it is respectfully requested that the references be entered into the record of the present application and that the Examiner place his or her initials in the appropriate area on the enclosed Form PTO-1449, thereby indicating the Examiner's consideration of each of the references.

The submission of the references listed on the Form PTO-1449 is for the purpose of providing a complete record and is not a concession that the references listed thereon are prior art to the invention claimed in the patent application. The right is expressly reserved to establish an invention date earlier than the above-identified filing date in order to remove any reference submitted herewith as prior art should it be deemed appropriate to do so. Further, the submission of the references is not to be taken as a concession that any reference represents art that is relevant or analogous to the claimed invention. Accordingly, the right to argue that any reference is not properly within the scope of prior art relevant to an examination of the claims in the above-identified application is also expressly reserved. The references were previously cited in the pending parent application Serial No. 60/251,347, filed December 04, 2000.

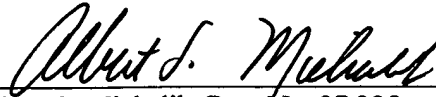
The Information Disclosure Statement is being filed after the mailing of the first Office Action on the merits, but before the mailing date of a final action under 37 C.F.R. § 1.113 or a Notice of Allowance under 37 C.F.R. § 1.311 and includes the Certification under 37 C.F.R. § 1.97(e) (see "Certification" below). Accordingly, no fee is owed by applicant. If, however, a fee is due, the Patent Office is hereby authorized to charge any fee associated with this submission to Deposit Account 50-1618.

**CERTIFICATION**

The undersigned attorney hereby certifies that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a

In re Application of WILLMAN, Bryan M.  
Serial No. 09/915,628

counterpart foreign patent application or, to the knowledge of the attorney signing after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

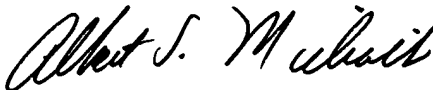


Albert S. Michalik, Reg. No. 37,395  
Attorney for Applicants  
Law Offices of Albert S. Michalik, PLLC  
704 – 228<sup>th</sup> Avenue N.E., Ste. 193  
Sammamish, Washington 98074  
(425) 836-3030 (telephone)  
(425) 836-8957 (facsimile)

CERTIFICATE OF MAILING

I hereby certify that this INFORMATION DISCLOSURE STATEMENT, PTO form 1449 and copies of the cited references are being deposited with the United States Postal Service on the date shown below, with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

Date: February 18, 2005



Albert S. Michalik

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
2721SERIAL NO.  
09/915,628INFORMATION DISCLOSURE STATEMENT  
BY APPLICANTAPPLICANT  
WILLMAN, BRYANFILING DATE  
July 26, 2001GROUP  
2127

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO*

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AA	Frailey et al. "Software Support for Modern Microprocessor Memory Systems." Midcon Midwest Electron Show Dec 12-14 1978, PP 1-15.
	AB	Blumrich, et al. "Virtual-Memory-Mapped Network Interfaces." February 1995 IEEE Micro, PP 21-28.
	AC	Tanner, Daniel J. "Clearing Up The Confusion: Virtual vs. Mapped Memory." Computer Design, Oct 1976, PP101-105.
	AD	Teller, et al. "Locating Multiprocessor TLBs at Memory." Proceedings of 27 <sup>th</sup> Hawaii International Conference on Systems Sciences, 4-7 Jan. 1994, PP 554-63.
	AE	Szytner, et al. "A New Virtual-Memory Implementation for UNIX." Usenix Association Summer Conference Proceedings, 9-13 June 1986, PP 81-92.
	AF	Baskett, F. "Pascal and Virtual Memory in a Z8000 or MC6800 Based Design Station." Compcon '80 VLSI: New Architectural Horizons, 25-28 Feb. 1980, PP 25-28.
	AG	Hulme, B.B. "Memory Management with the Z80000 Microprocessor." Midcon Conference, 1-17 Sept. 1987, PP 86-91.
	AH	Kaplinsky, Cecil. "Memory Access Paths: Hierarchies and Protection." Wescon Conference 1984, PP 1-7
	AI	Kotz et al. "The Expected Lifetime of Single-Address-Space: Operating Systems." Sigmetrics 94, PP 161-170.
	AJ	Kuei et al. "Efficient Index Generation for Compiling Two-Level Mappings in Data-Parallel Programs." Journal of Parallel and Distributed Computing, Vol. 60, no.2; Feb 2000, PP 189-216.